

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

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Docket Number (Optional)

56231-417 (MKS-131)

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10/669,065

Applicant(s)

Ali Shajii et al.

Filing Date

September 23, 2003

Group Art Unit

2855

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

93		Zhiyao Huang et al., "Application of Electrical Capacitance Tomography to the Void Fraction Measurement of Two-Phase Flow", <i>IEEE Transactions on Instrumentation and Measurement</i> , Vol. 52, No. 1, February 2003, pp. 7-12.
93		Kjetil Ullaland, "The Design of a VLSI Mixed Analogue and Digital Modular ASIC Processor for Capacitance Tomography", October 1994, University of Bergen, Bergen, Norway, pp. 4.1 - 4.6.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.